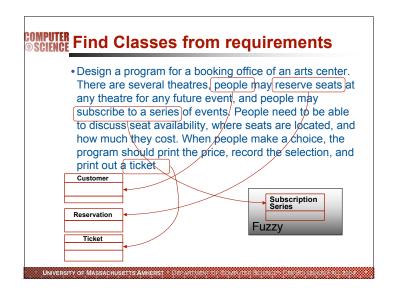
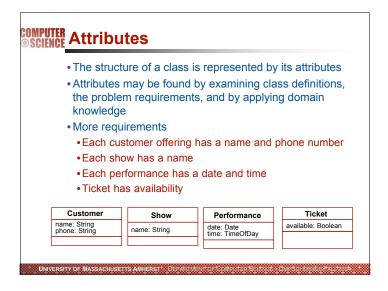
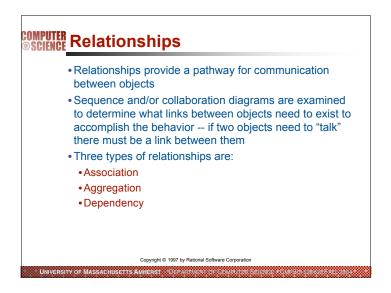


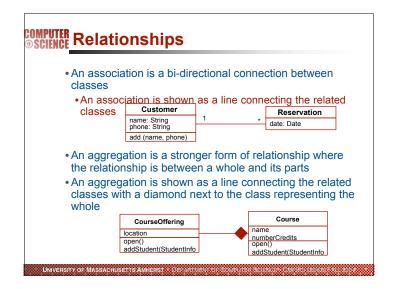
# A class is a collection of objects with common structure, common behavior, common relationships and common semantics Classes may be found by examining: written requirements objects in sequence and collaboration diagrams A class is drawn as a rectangle with three compartments name attributes operations Classes should be named using the vocabulary of the domain Naming standards should be created e.g., all classes are singular nouns starting with a capital letter

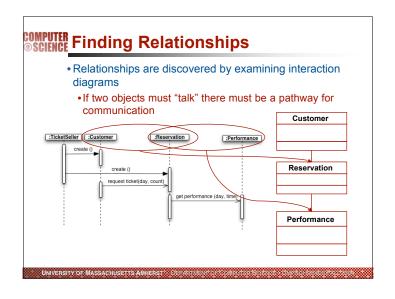


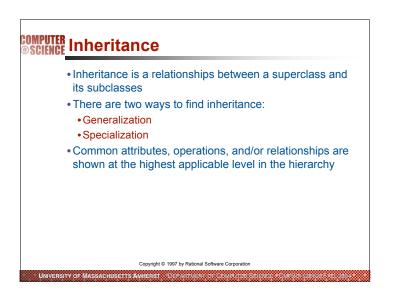


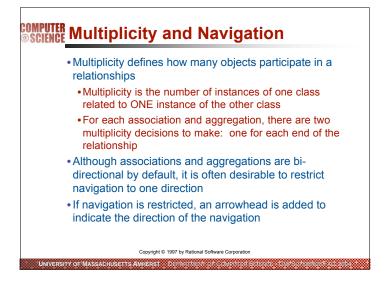


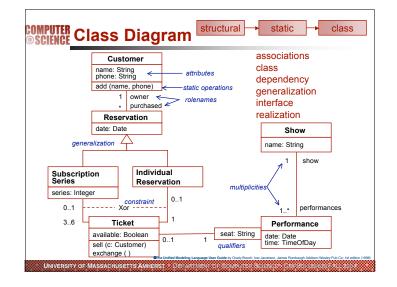
# Operations The behavior of a class is represented by its operations Operations may be found by examining interaction diagrams UNIVERSITY OF MASSACHUSETTS AMPERED

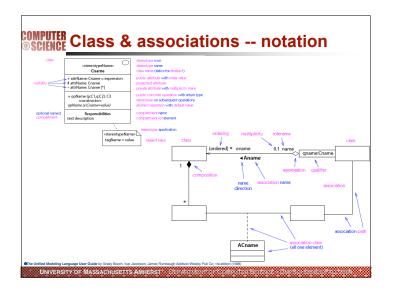


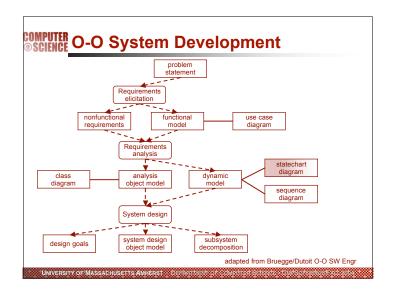


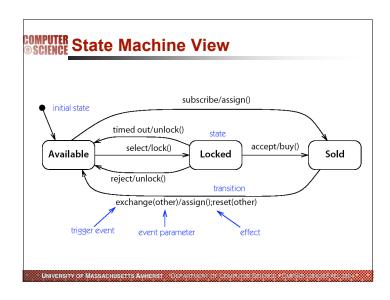


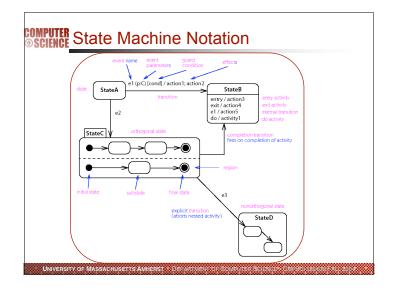


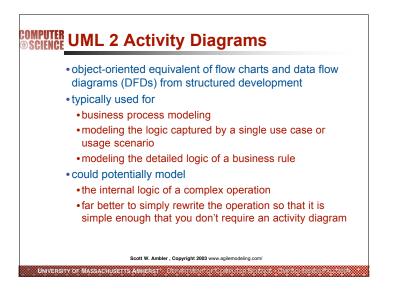


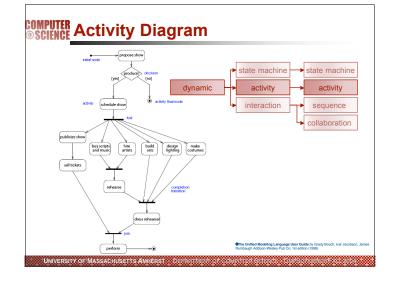


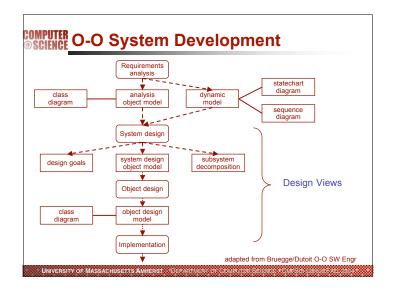


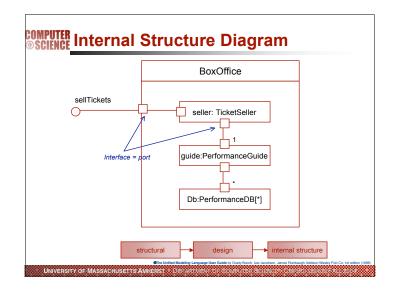


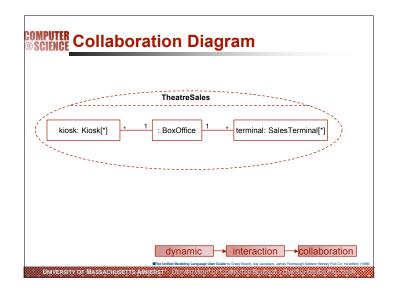


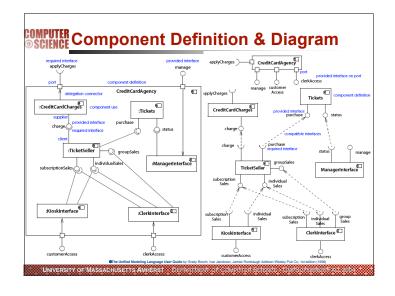


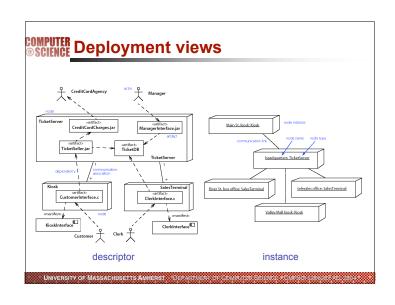


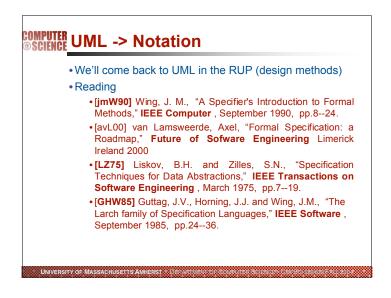


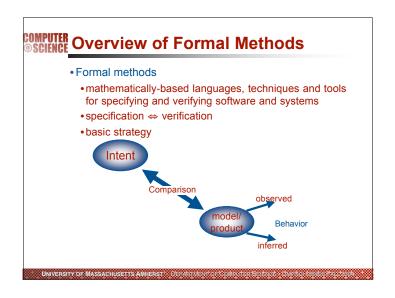


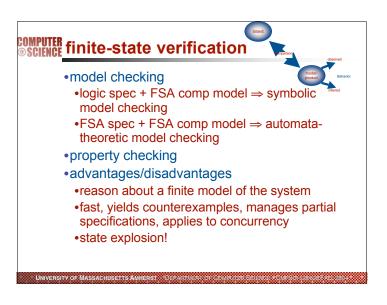












## COMPUTER Basic Verification Strategy

- analyze a system for desired properties, i.e., compare behavior to intent
- intent
- can be expressed as properties of a model (model-based specification)
- can be expressed as formulas in mathematical logic (property-based specification)
- behavior
  - can be observed as software executes
  - can be inferred from a model
  - can be expressed as formulas in mathematical logic
- different representations support different sorts of inferences

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# COMPUTER (automated) mathematical reasoning

- theorem proving
- proof checking
- advantages/disadvantages
- •difficult, error prone
- decidability vs. expressiveness
  - •propositional calculus is decidable
  - predicate calculus is semi-decidable

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#### COMPUTER Specifications

- · define intent and provide a basis for formal reasoning
- · should be based on a sound mathematical theory
- criteria to evaluate specification methods (languages)
- mathematical foundation
- constructability (ease of use)
- comprehensibility
- minimality
- general applicability
- extensibility

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### COMPUTER Properties

- a specification syn ∈ Syn is **unambiguous** if and only if Sat maps syn to exactly one specificand set.
- a specification syn ∈ Syn is consistent (or satisifable) if and only if Sat maps syn to a non-empty specificand set.
- Given <Syn, Sem, Sat >, an implementation prog ∈ Sem is correct with respect to a given specification spec ∈ Syn if and only if Sat (spec, prog)
- informally, a specifier who "overspecifies" is guilty of "implementation bias"
- a specification has implementation bias if it specifies unobservable properties of its specificands,
- e.g., a set specification that keeps track of the insertion order favors an ordered-list implementation over a hash table implementation

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### COMPUTER What is a specification language?

- A formal specification language is a triple
- <Syn, Sem, Sat >, where Syn and Sem are **sets** Syn X Sem D Sat is a relation.
- Given a specification language, <Syn, Sem, Sat>
- •if Sat (syn, sem) then syn is a **specification** of sem and sem is a **specificand** of syn
- the specificand set of a specification syn ∈ Syn is the set of all specificands sem ∈ Sem, such that Sat (syn.sem)

#### from Wing

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# COMPUTER Classification © SCIENCE

- Model-oriented (operational) specification
  - behavior described in terms of another data abstraction or mathematical model with known properties, e.g., tuples, relations, functions, sets, and sequences
- Property-oriented (descriptive) specification
  - behavior is described in terms of properties, usually stated as axioms, that the system must specify
  - or the objects and operations to define themselves implicity
- · Formal vs "semi-formal" vs informal

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### COMPUTER Alternative classification

- Axiomatic specification
- Abstract models
- Set Theory
- Predicate Logic
- Programming Languages

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# COMPUTER Semi-Formal Technques

- . Communication: DFD
- lack precise semantics
- abstract "machine" for interpreting the operational semantics of a DFD specification is not fully defined
- · can't simulate behavior
- Behavior: FSA
- limited memory
- combinatorial explosion

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# COMPUTER Model-oriented examples

- · Formal:
- Abstract-data-type specification languages: Parnas' state machines, VDM, Z
- Concurrent and distributed systems specification languages: Trace Specifications, Petri nets, CCS, CSP
- Semi-Formal
- Diagrams
  - · Behavior: FSA, Petri-Nets, StateCharts
  - Communications: DFD, activity diagrams, sequence diagrams
- Functions: Use-Case diagrams

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# COMPUTER abstract data type example

type stack is create: ⇒ stack pop: stack ⇒ stack

push: stack X integer  $\Rightarrow$  stack

top: stack  $\Rightarrow$  integer

Note: Because some of the specification methods are easier to apply to functions, all operations are functions

idilottolio

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```
• type definition:

type S is record

top: integer

data: array [1 ... ] of integers

end record

• operational specification:

{true} push (S₀, I) ⇒ S

{∀ J, 1 < J ≤ S₀.top

S₀.data [J] = S.data [J] ∧

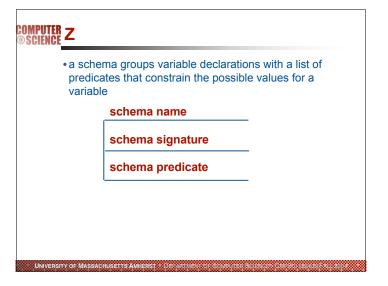
S.top = S₀.top + 1 ∧

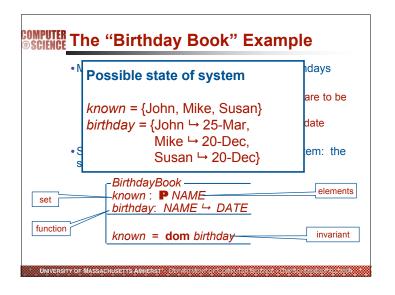
S.Data [S.top] = I}
```

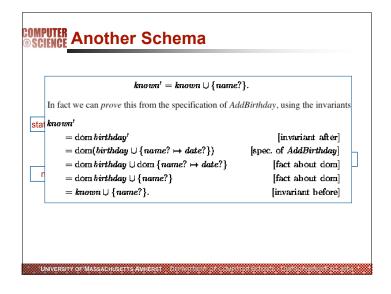
```
Proposed by Abrail, 1980

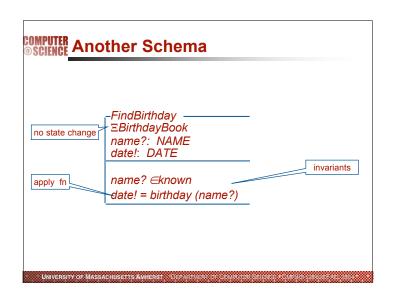
developed by Hayes and Spivey
based on typed set theory and first order logic
provides a schema to describe a specifications state and operations
describe systems as collections of SCHEMAS
inputs and outputs to functions
Invariants: statements whose truth is preserved by the functions
```

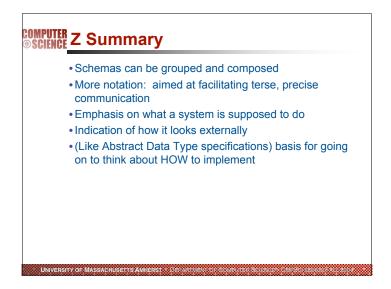
```
• ordered set definition:  X = \{x_0, x_1, \dots, x_n\}   |X| = n+1   extract(X) = \{x_0, x_1, \dots, x_{n-1}\}  • operational definitions:  create = \{0\}   push (S_0, I) = S \Lambda   S_0 = extract(S) \Lambda   |S| = |S_0| + 1 \Lambda   x_{|S|} = I
```











# COMPUTER State machine model

- 2 types of operations
  - V-Operations (value returning)
    - · Do not cause a change in state
  - O-Operations
    - Cause a change in state
- specs must show the effect of each operation on the Voperations

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# COMPUTER Hidden Operations

- must deal with side effects and delayed effects, such as the effect of PUSH on TOP
- V-operation: DEPTH

possible values: integer; initial value 0

parameters: none effect: none

 Parnas had informal language, later hidden operations were used to support the provided O & V operations. In both cases, need to show that 0≤ Depth (S) ≤ MAX

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# COMPUTER Example

- V-operation: TOP
  - possible values: integers; initially undefined
  - parameters: none
  - effect:
    - error call if 'DEPTH' = 0
- O-operation: PUSH(a)
  - possible values: none
  - parameters: integer a
  - епест:
    - error call if 'DEPTH' = MAX
    - else (TOP =a; 'DEPTH' = 'DEPTH'+1)

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# COMPUTER Concurrent & distributed systems

- •FSA
- Petri nets
- Trace specifications
- •a trace is a sequence of procedure or function calls and return values from those calls
  - proposed by David Parnas, 1977
  - formalized by McLean, 1984
  - further developed by Dan Hoffman, Rick Snodgrass, etc

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#### COMPUTER Finite State Machines (FSM's)

- FSM's describe behavior of a system:
- The sequence of stages/steps/conditions that the system goes through
- •FSM shows how a system acts/reacts to inputs
- Does this by showing progress through different states
- Hypothesis:
- The universe in which the system being described must operate can be accurately modeled as always being in exactly one of a finite number of states (situations)
- •There are only a finite number of possible system inputs

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### COMPUTER Why Use FSM's?

- Primary appeal is visualization
- Intuitively: Can "watch" a stream of inputs "drive" the behavior of the system as a sequence of movements from state to state
- Kinds of questions FSM's seem adept at helping answer:
- •"What is a good way to think about the problem to be solved?"
- "What is the solution approach?"
- "How does this program work?"

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# COMPUTER Finite State Machines (FSM)

- finite set of states S ={s<sub>1</sub>, ..., s<sub>n</sub>}
- finite set of inputs  $I = \{i_1, ..., i_n\}$
- transition function  $\delta$ : SxI  $\Rightarrow$  S
  - can be a partial function
- represent as a graph
  - nodes ⇒ states
  - $\bullet$  edges  $\Rightarrow$  inputs
- graph ⇒ transition function

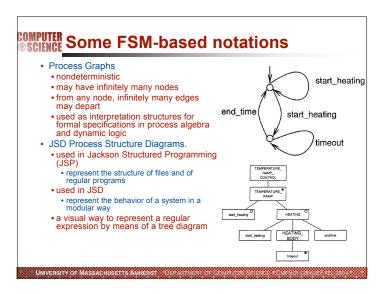
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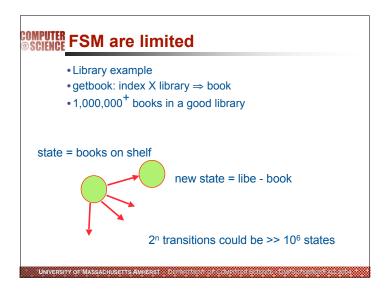
# COMPUTER Enhancements to FSM's

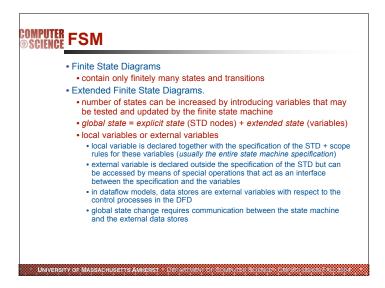
- Use of hierarchy
- Output annotations on edges
- Distinguished Initial and Terminal states
- Separate data definitions, local and global variables

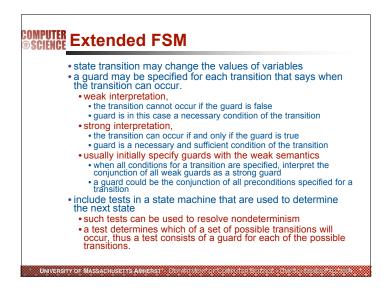
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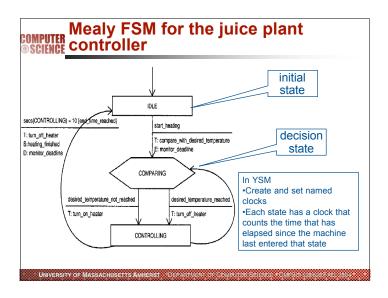
# Procus on specific issue: safety concern Model unsafe state Model state transitions Can unsafe state be reached? Drawbacks No sense of functionality No sense of how functionality achieved Difficult and generally impossible to reason about timing











# Mealy & Moore Machines Mealy machine output actions are associated with transitions Moore Machines outputs are associated with states UNIVERSITY OF MASSACHUSETTS AMPERS

